



INDIAN INSTITUTE OF TECHNOLOGY PATNA
BIHTA PATNA-801106
RESEARCH & DEVELOPMENT UNIT

ADVERTISEMENT NO: R&D/908/MEM/328

DATED: 19.12.2023

Project No. **R&D/SP/EE/MEM/2023-24/908**

Applications are invited in the prescribed format only for the following assignment in a purely time bound research project undertaken in this institute.

1. (a) Name of the temporary assignment : Junior Research Fellow (JRF)

(b) Number of Post : 03 (Three)

(c) Duration of the Post : 02 (Two Years), thereafter Senior Research Fellow (SRF) till completion of the project
2. Name of the temporary research project : ML Enabled RISC-V based i-LoRa SOC for Forest Event Monitoring
3. Name of the sponsoring Agency : Ministry of Electronics and Information Technology (MeitY), Delhi under C2S Programme
4. Consolidated Fellowship/Salary: Rs. 37000 (for JRF) and Rs. 42000 (for SRF)
5. Qualification : M.Tech/ B. Tech (Electrical /Electronics/ Communication/ Instrumentation / Computer Science & Engineering)

The candidate desirably should have M.Tech.(VLSI/Embedded System/ Microelectronics/ Electronics System/Communication System/ Computer Science Engineering/Allied discipline) degree with a minimum CPI of 7.0 or 70% of marks with qualified GATE Score (within last five years) **OR** B.Tech. (Electrical/ Electronics/ Communication/ Instrumentation/ Computer Science & Engineering) degree with a minimum CPI of 7.5 or 75% of marks with qualified GATE Score (within last five years). GATE score shall be exempted for candidates having B.Tech degree with minimum CPI 7.0 from IITs. Exceptional candidates having M. Tech degree and minimum two (02) years of relevant experience, may be offered SRF Position directly based on the performance in interview. The upper age limit for applying for the JRF position shall be **32 and 28 years** for candidates having M.Tech & B.Tech. degree respectively.

Relaxations for SC/ST/OBC/women/PD will be given as per the GOI rules. Working/ research experience in the field of **VLSI and Embedded System/ Computer Architecture/ Communication System/Machine Learning and exposure to EDA tools** shall be given preference.



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Interested and eligible candidates may send the scan copy of application form (in appended format) along with resume highlighting the relevant exposure in the domain of the project to the mail id: kcr@iitp.ac.in, on or before 13th January 2024. Shortlisted candidates shall be intimated on 15th January 2024 and called for the **PERSONAL INTERVIEW** to be scheduled on 30th Jan 2024, Timing 10 AM at Dept. of Electrical Engineering, IIT Patna, Bihta, Patna – 801103 with updated resume along with the original copies of all supporting documents (certificates, mark-sheets, and GATE Score card).

Please note that those candidates attend physically shall be entertained to get reimbursed onward & return 2nd class sleeper train fare only in shortest route. No DA is admissible for appearing in the interview. Guest room in student hostel may be arranged on prior intimation through mail for maximum two days only.

Candidates with prior intimation and valid reason may attend interview online on the same day. Timing of interview through online shall be intimated through mail. No TA/DA is admissible for appearing online interview. Application forms through any other media shall not be entertained.

Important Date & Time:

Last date for receiving the application form through mail: 13th January 2024

Date of intimation of shortlisted candidates through mail: 15th January 2024

Date of Personal / Online Interview : 30th January 2024

Please Note that, candidates those join project as JRF/SRF shall be encouraged to pursue PhD in Electrical Engineering Department during July 2024 session/ later subject to the fulfil of the PhD admission protocol of the department and institute.

For any query, please contact Principal Investigator of the Project: **Dr. Kailash Chandra Ray, Dept. of Electrical Engineering, IIT Patna, Email: kcr@iitp.ac.in, Tel.: 06115-233106 (Office).**

Deputy Registrar

Copy to:

1. Associate Dean, R&D, IIT Patna
2. Advertisement file
3. Project file
4. PIC (Outreach) to put it in institute social media channel



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FORMAT OF APPLICATION FOR JRF

Name & Address Including email id and Phone no. (for Correspondence)	Category (GEN/OBC/SC/S T/PD)	DOB dd/ mm/yy	Professional Exam. (GATE/CSIR-NET, etc) & Validity
NAME IN CAPITAL Address: Phone: Email:			
Educational Qualification			
Institute/ Board	Exam Passed	Year of Passing	% of Marks/CPI
	10 th Class		
	12 th Class		
	Bachelors (B.Sc/B.Tech/B.E./BCA) or equivalent		
	Masters (M.Sc/M.Tech/M.E/MCA/ MA) or equivalent		

Qualifying degree	Degree/ major/Specialization
(B.Sc/B.Tech/B.E./BCA)	
(M.Sc/M.Tech/M.E/MA/MCA)	
Others	

Signature of applicant

Date:

Place: